

Docket No.: M4065.0424/P424
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
William E. Corr

Group Art Unit: 2858

Application No.: 09/808,140

Examiner: Anjan K. Deb

Filed: March 15, 2001

Allowed: April 8, 2005

For: METHOD AND APPARATUS FOR
MEASURING ON-CHIP POWER SUPPLY
INTEGRITY

Issue Fee Paid: July 8, 2005

**COMMENTS ON EXAMINER'S
STATEMENT OF REASONS FOR ALLOWANCE**

U.S. Patent and Trademark Office
Customer Window, Mail Stop Issue Fee
Randolph Building
401 Dulany Street
Alexandria, VA 22314

Dear Sir:

While Applicant agrees in part with the Examiner's stated reasons for allowance, Applicant notes that the allowed claims define further unique combinations of limitations not found in the prior art and not indicated in the Examiner's stated reasons. Therefore, the stated reasons for allowance should be interpreted as highlighting only some of the reasons why the claims are allowable.


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As usual, the scope of the claims should be interpreted based on the actual language of the allowed claims, and no further limitation of the claims should be inferred from the Examiner's Statement of Reasons for Allowance.

Dated: July 8, 2005

Respectfully submitted,

By 

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